

Table of Contents

1	Low Power Low Voltage High Speed Circuit Design Using Strained Silicon <i>by Prolay Ghosh</i>	1-7
2	A unique approach for Market Basket Analysis under the framework of Probability Based Association Rule Learning <i>by Abhijit Sarkar, Apurba Paul, Anupam Mondal and Abhijit Ghosh</i>	8-14
3	A 4.596 GHz, High Slew Rate, Ultra low Power Cascode Operational Amplifier in 45nm CMOS for Wireless Communication <i>by Ramkrishna Kundu, Abhishek Pandey, Dipayan Ghosh, Jyoti Singh and Vijay Nath</i>	15-20
4	Indian Coin Recognition using Image Subtraction Technique <i>by Sabita Pal, Gaurav Kumar and Sovan Raj Meher</i>	21-30
5	Circularly Polarized Square MS Patch Antenna Design with two Corners Chopped for ISM Band <i>by M.S. Kasbe, T. H. Mujawar, V. D. Bachuwar, S. A. Pawar and S. S. Suryavanshi</i>	31-35
6	QoS Provisioning of QoS in Wireless Networks carrying multimedia traffic <i>by Ritwika Choudhuri, Anindya Ghosh, Sreeparna Nandi and Antara Chowdhury</i>	36-43